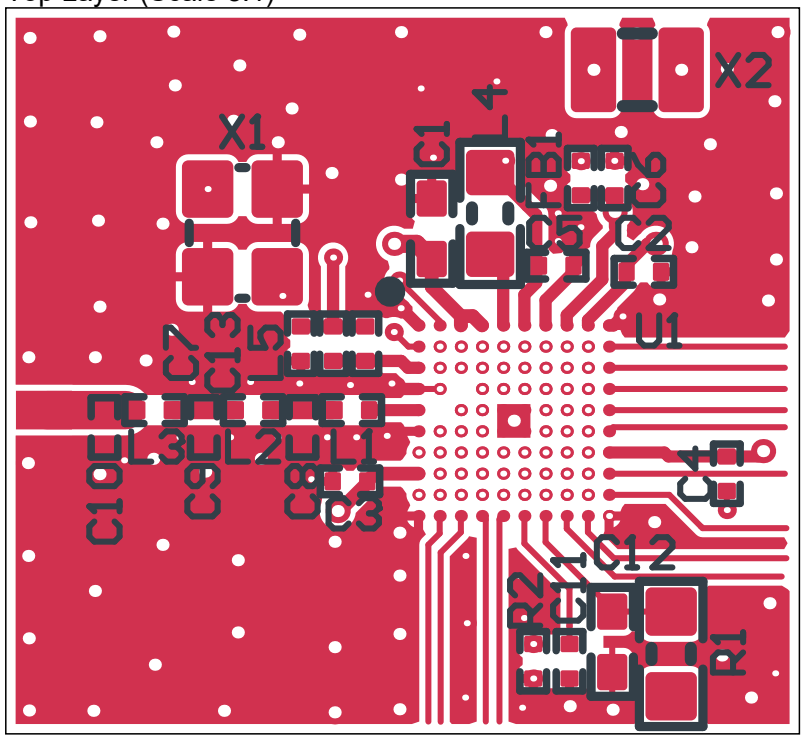
Reference layout revisions earlier than 1.0 can be changed without prior notice.
Always refer to Nordic Semiconductor documentation for the latest information
on recommended reference layout files before starting your design.

Top Layer (Scale 8:1)



NOTE:

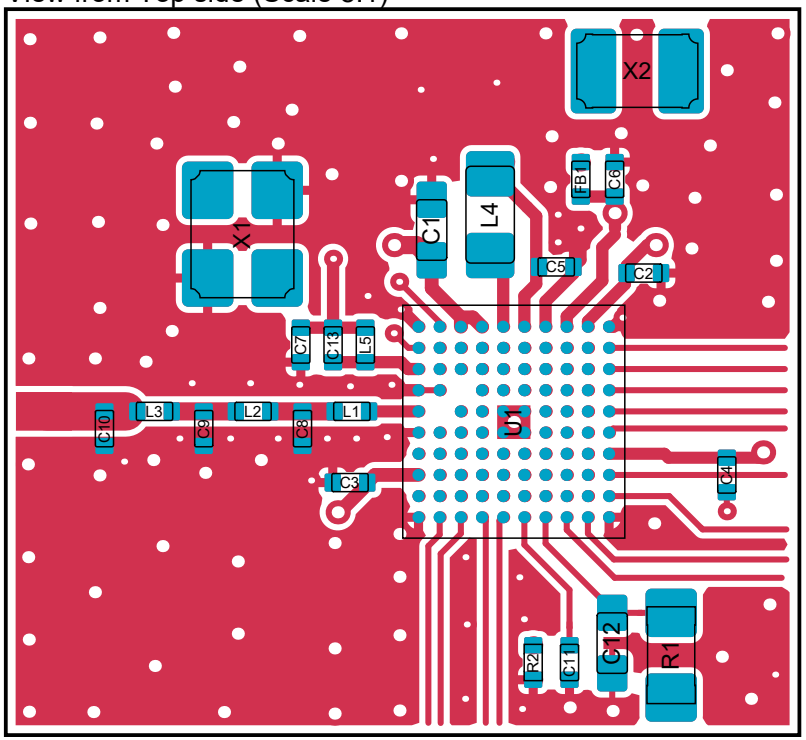
* Place matching network components as close to the U1's ANT pin as possible.
* Prepreg thickness between Top Layer and Mid-Layer 1 shall be ≤ 100 µm.

Nordic Semiconductor does not warrant the accuracy or completeness of this documentation or any included information. Nordic Semiconductor makes no assurances that this design will meet specification, be appropriate for your application, or fit for any particular purpose, or that it will function in an implementation. Nordic Semiconductor also does not claim that the design is ready for production. It is your responsibility to thoroughly validate and test your design implementation to confirm the functionality of your application.
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Title: nRF54LM20B-PAAA Reference Layout, 8 layer PCB

Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0
Date: 2026-11-19			Sheet 1 of 17
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf			
Classification: Public			

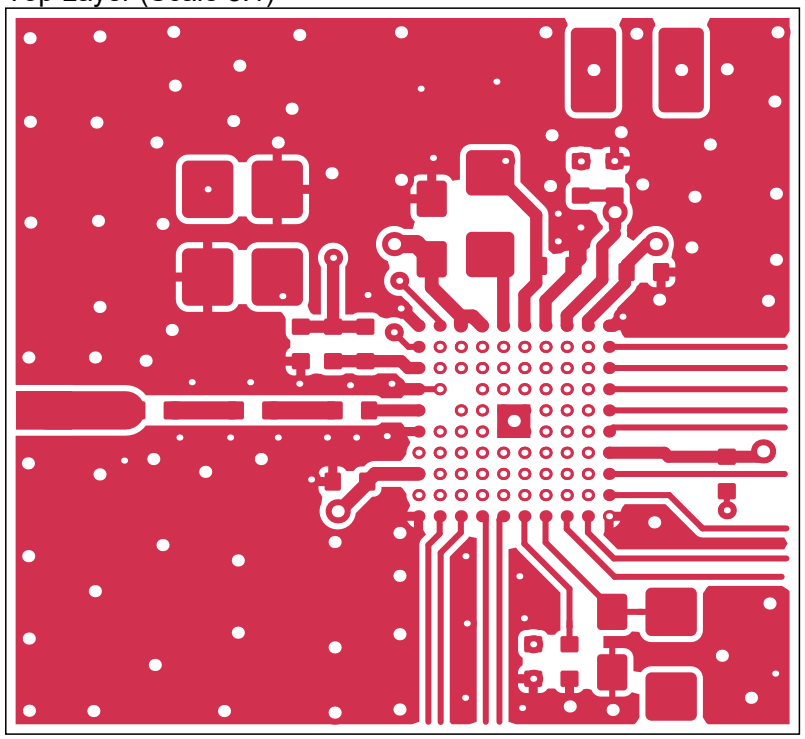
View from Top side (Scale 8:1)



Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 2 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

NORDIC SEMICONDUCTOR

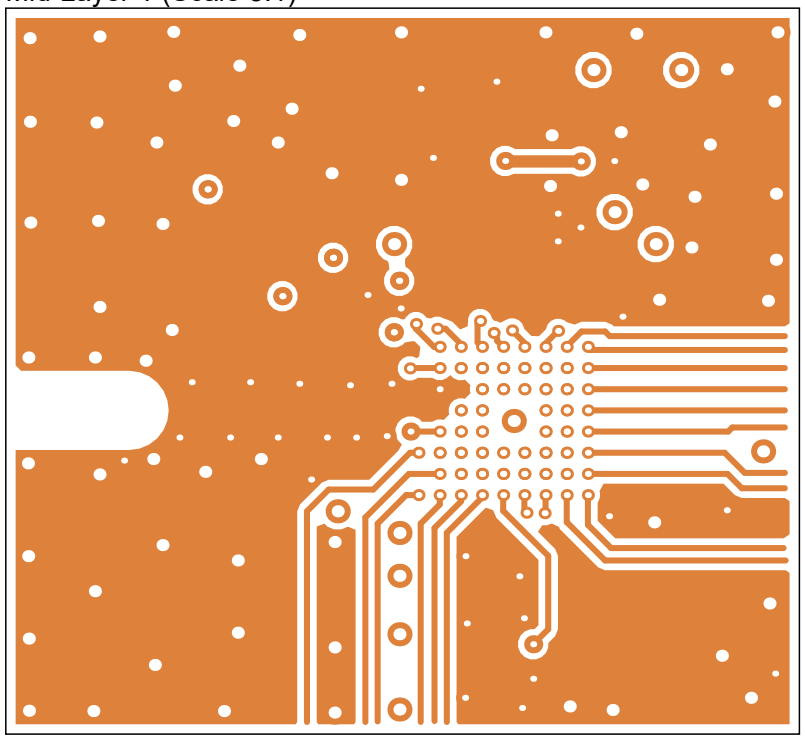
Top Layer (Scale 8:1)



Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 3 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

NORDIC SEMICONDUCTOR

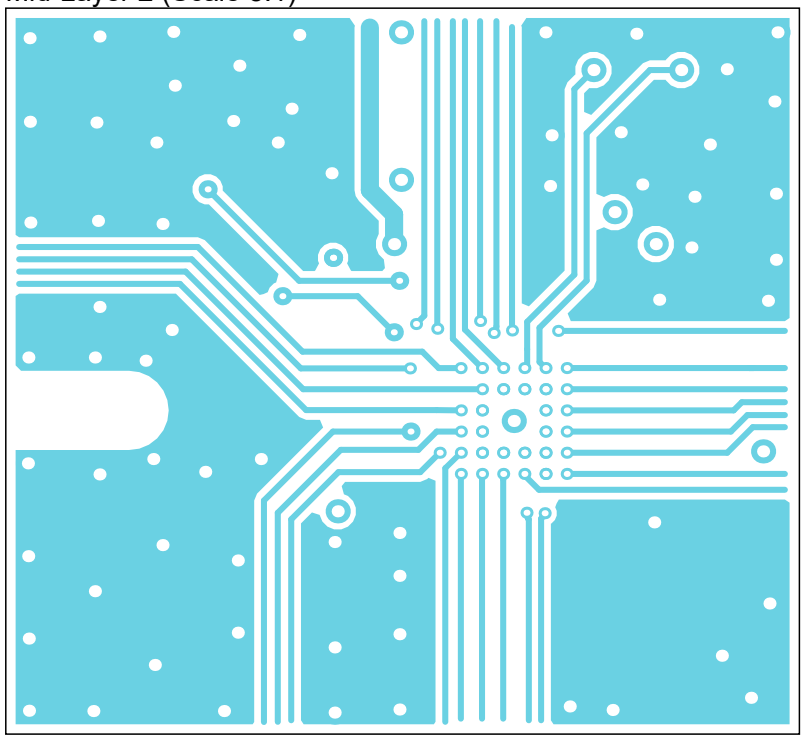
Mid-Layer 1 (Scale 8:1)



Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 4 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

NORDIC SEMICONDUCTOR

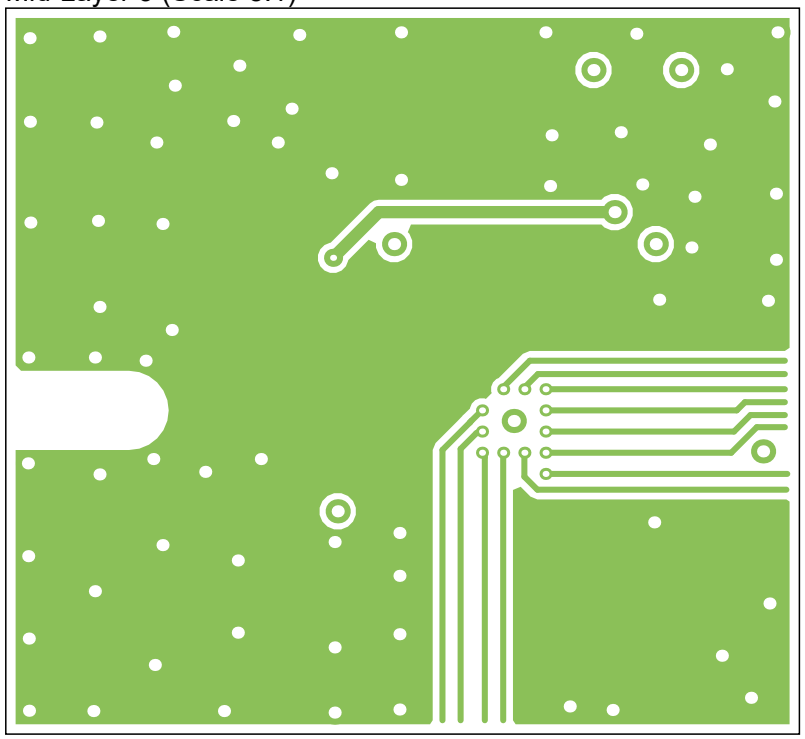
Mid-Layer 2 (Scale 8:1)



Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 5 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

NORDIC SEMICONDUCTOR

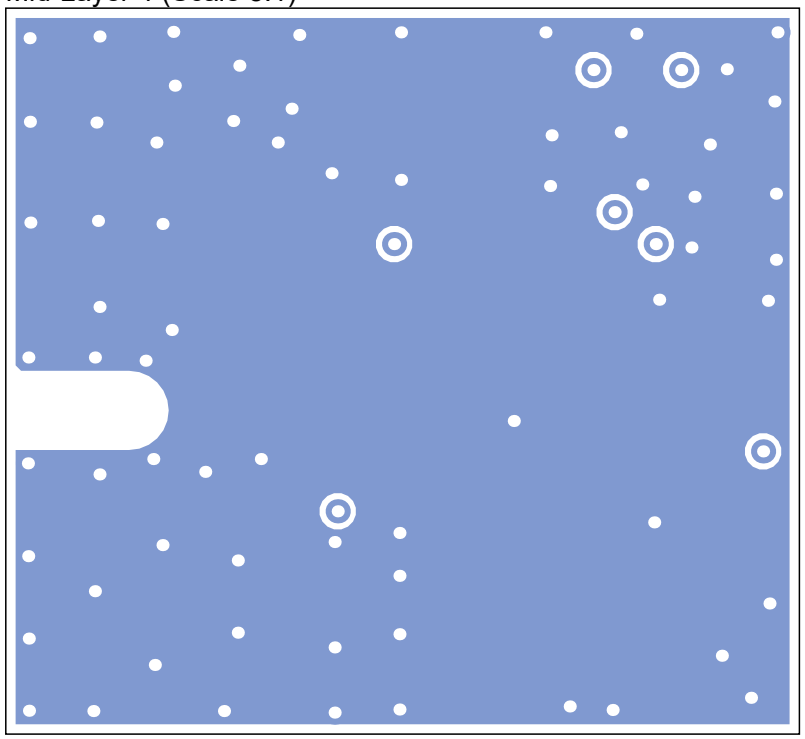
Mid-Layer 3 (Scale 8:1)



Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 6 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

NORDIC SEMICONDUCTOR

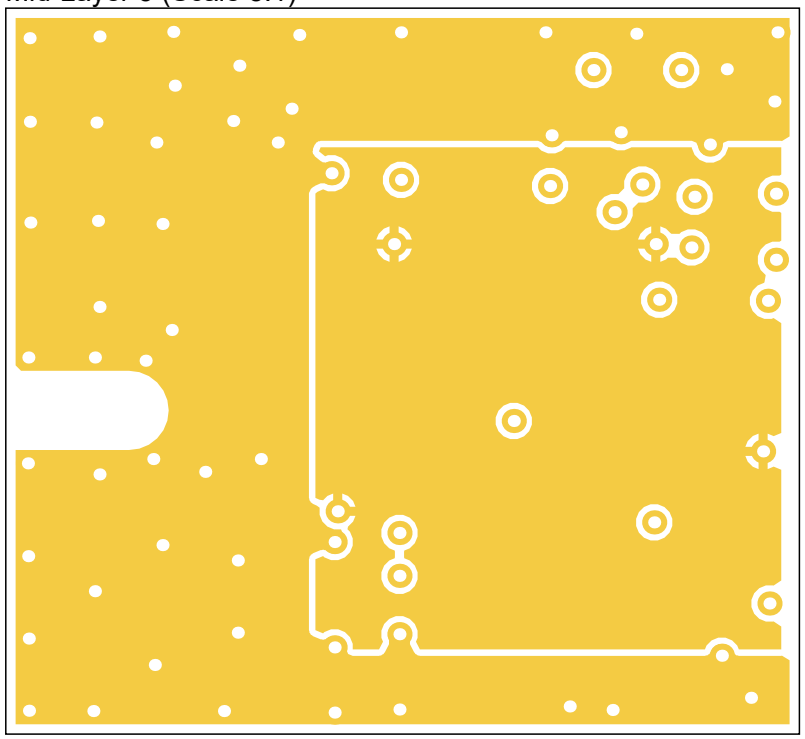
Mid-Layer 4 (Scale 8:1)



Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 7 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

NORDIC SEMICONDUCTOR

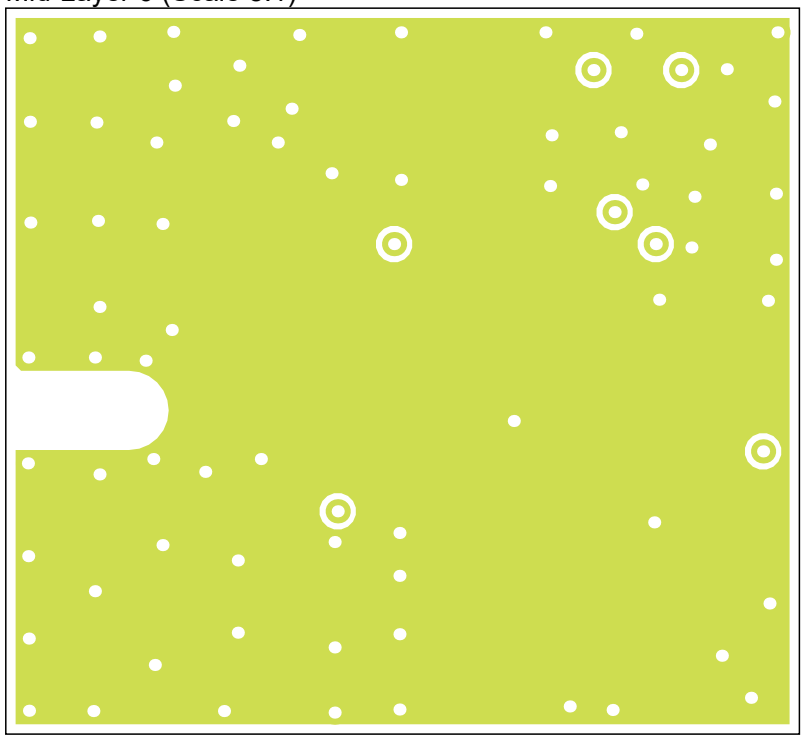
Mid-Layer 5 (Scale 8:1)



Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 8 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

NORDIC SEMICONDUCTOR

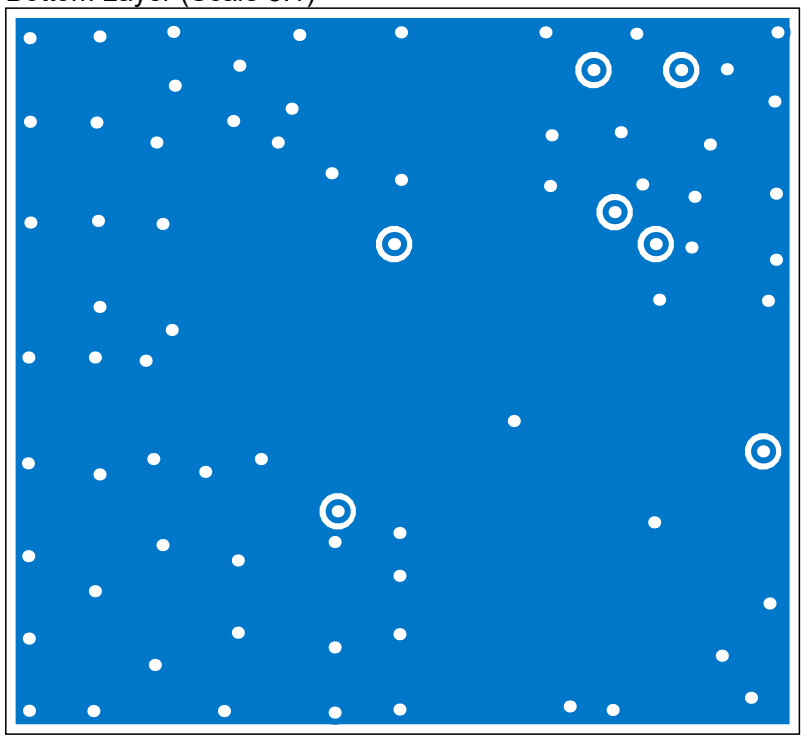
Mid-Layer 6 (Scale 8:1)



Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 9 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

NORDIC SEMICONDUCTOR

Bottom Layer (Scale 8:1)



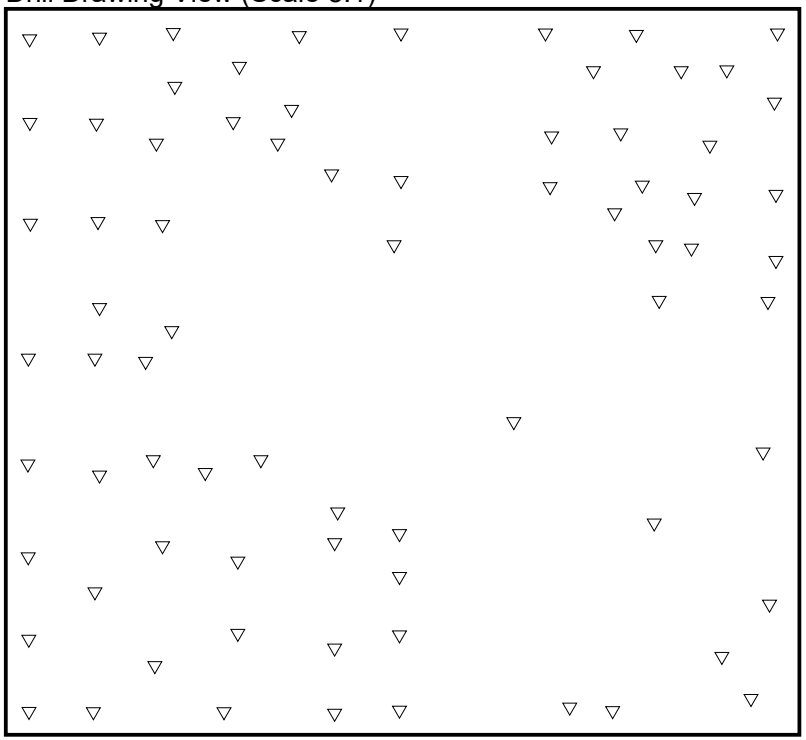
Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 10 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

NORDIC SEMICONDUCTOR

Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
▽	75	0.20mm	Plated	
	75 Total			

Drill Drawing View (Scale 8:1)



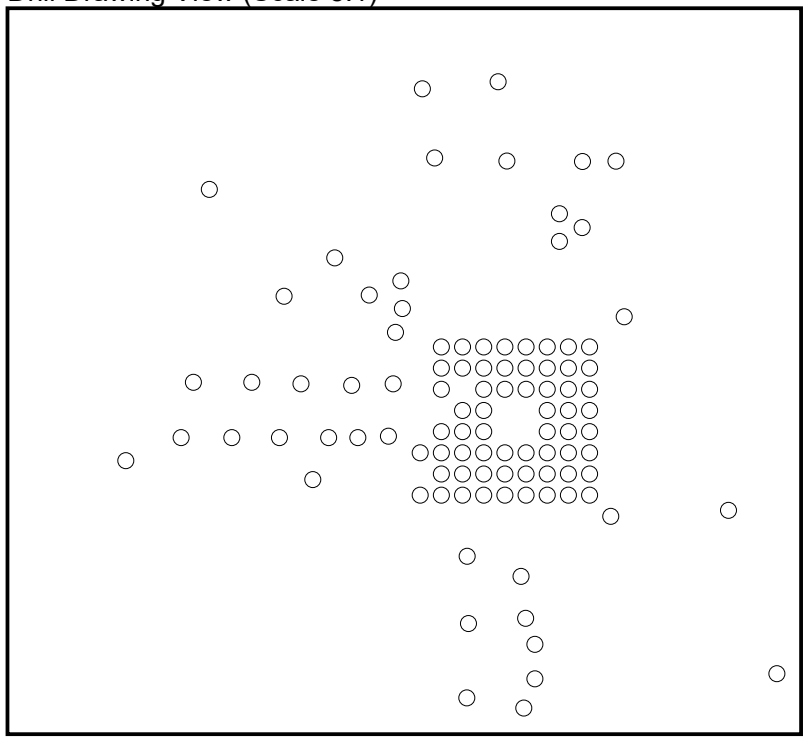
Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 11 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

NORDIC SEMICONDUCTOR

Drill Table

Symbol	Count	Hole Size	Plated	Drill Layer Pair
○	101	0.10mm	Plated	Top Layer - Mid-Layer 1
	101 Total			

Drill Drawing View (Scale 8:1)



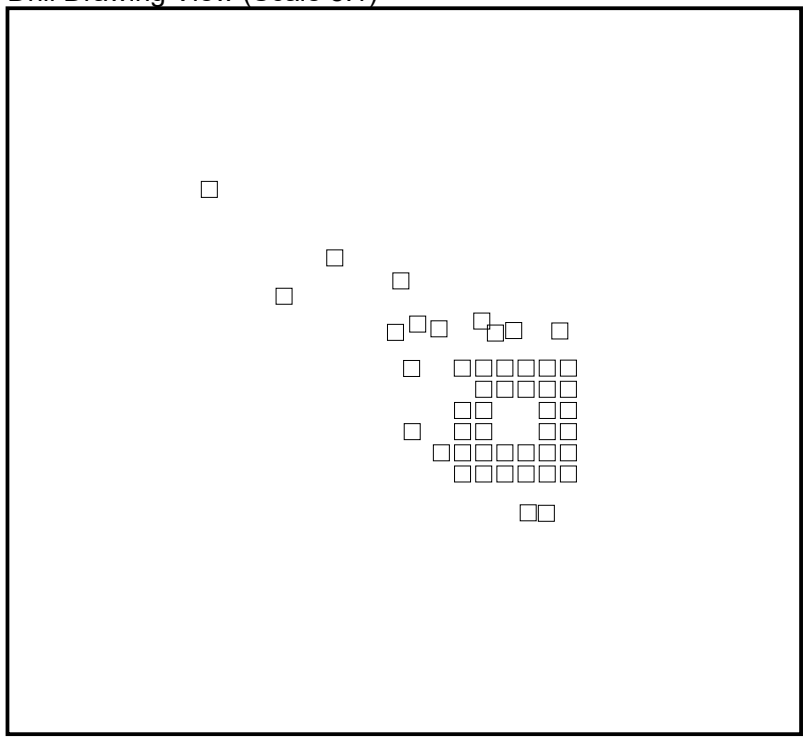
Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 12 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

NORDIC SEMICONDUCTOR

Drill Table

Symbol	Count	Hole Size	Plated	Drill Layer Pair
□	47	0.10mm	Plated	Mid-Layer 1 - Mid-Layer 2
	47 Total			

Drill Drawing View (Scale 8:1)



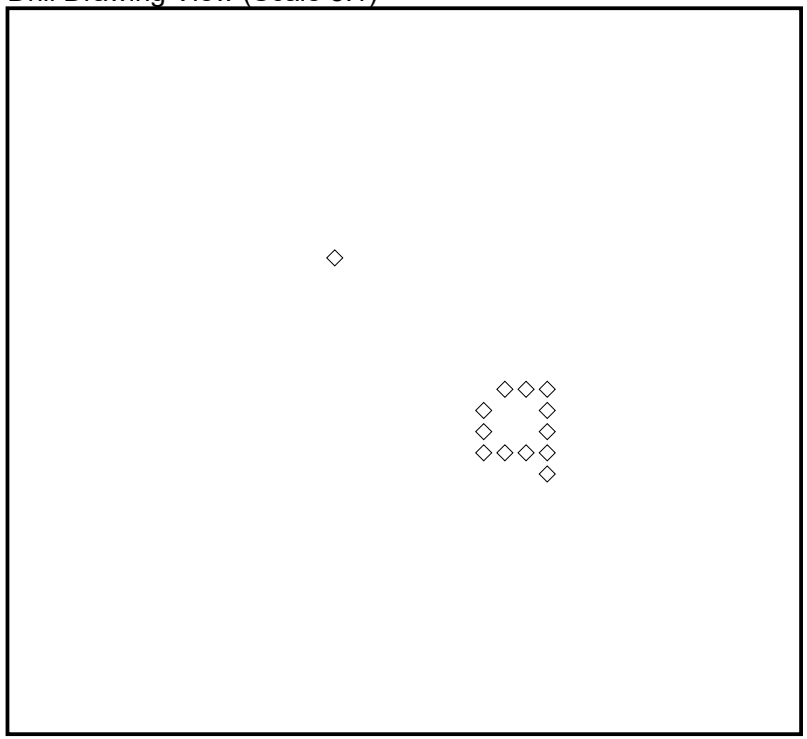
Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 13 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

NORDIC SEMICONDUCTOR

Drill Table

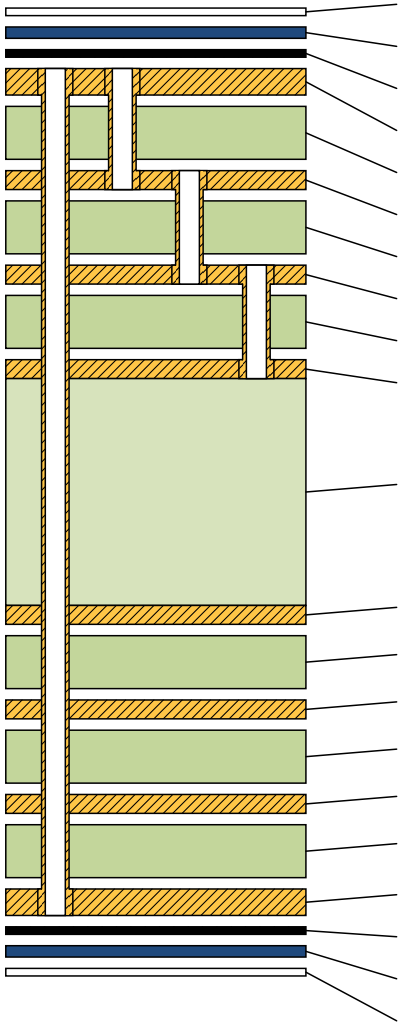
Symbol	Count	Hole Size	Plated	Drill Layer Pair
◇	13	0.10mm	Plated	Mid-Layer 2 - Mid-Layer 3
	13 Total			

Drill Drawing View (Scale 8:1)



Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 14 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

Layer Stack Legend



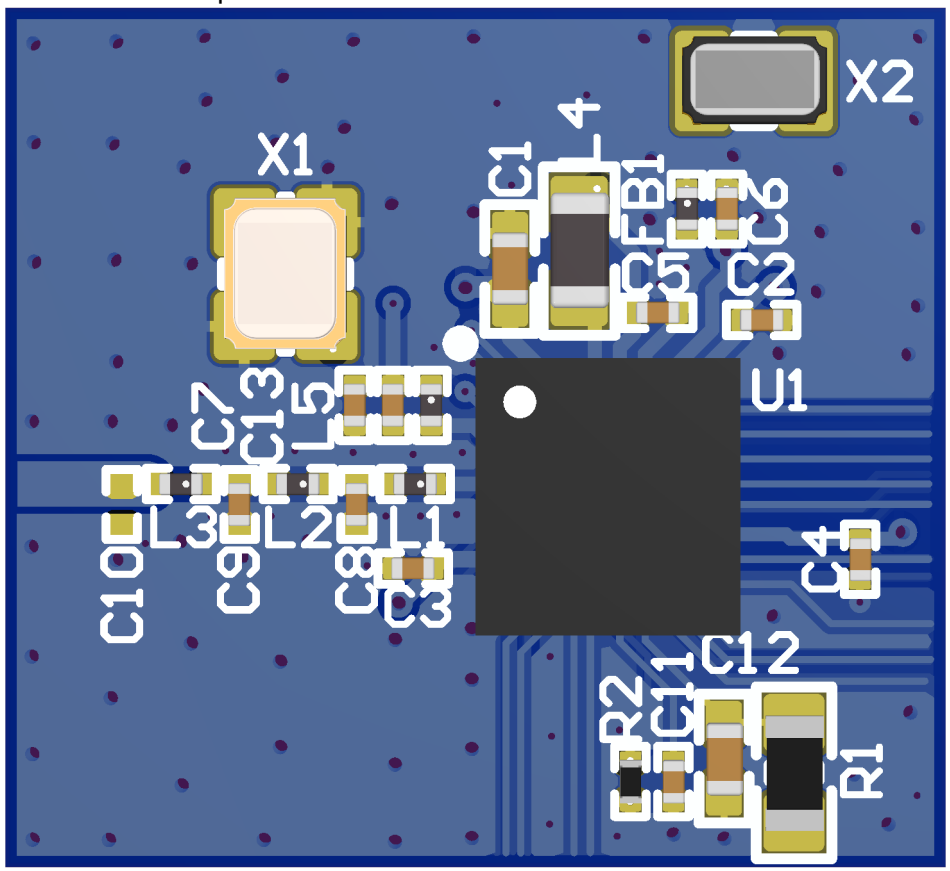
Layer	Thickness	Type	Gerber
Top Overlay		Legend	GTO
Top Solder	0.015mm	Solder Mask	GTS
Top Surface Finish	0.006mm	Surface Finish	
Top Layer	**0.035mm**	**Signal**	**GTL**
	0.070mm	*Dielectric*	
Mid-Layer 1	**0.025mm**	**Signal**	**G1**
	0.070mm	*Dielectric*	
Mid-Layer 2	**0.025mm**	**Signal**	**G2**
	0.070mm	*Dielectric*	
Mid-Layer 3	**0.025mm**	**Signal**	**G3**
	0.889mm	*Dielectric*	
Mid-Layer 4	**0.025mm**	**Signal**	**G4**
	0.070mm	*Dielectric*	
Mid-Layer 5	**0.025mm**	**Signal**	**G5**
	0.070mm	*Dielectric*	
Mid-Layer 6	**0.025mm**	**Signal**	**G6**
	0.070mm	*Dielectric*	
Bottom Layer	**0.035mm**	**Signal**	**GBL**
Bottom Surface Finish	0.006mm	Surface Finish	
Bottom Solder	0.015mm	Solder Mask	GBS
Bottom Overlay		Legend	GBO

Total thickness: 1.570mm

Title: nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0
Date: 2026-11-19			Sheet 15 of 17
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf			
Classification: Public			

NORDIC SEMICONDUCTOR

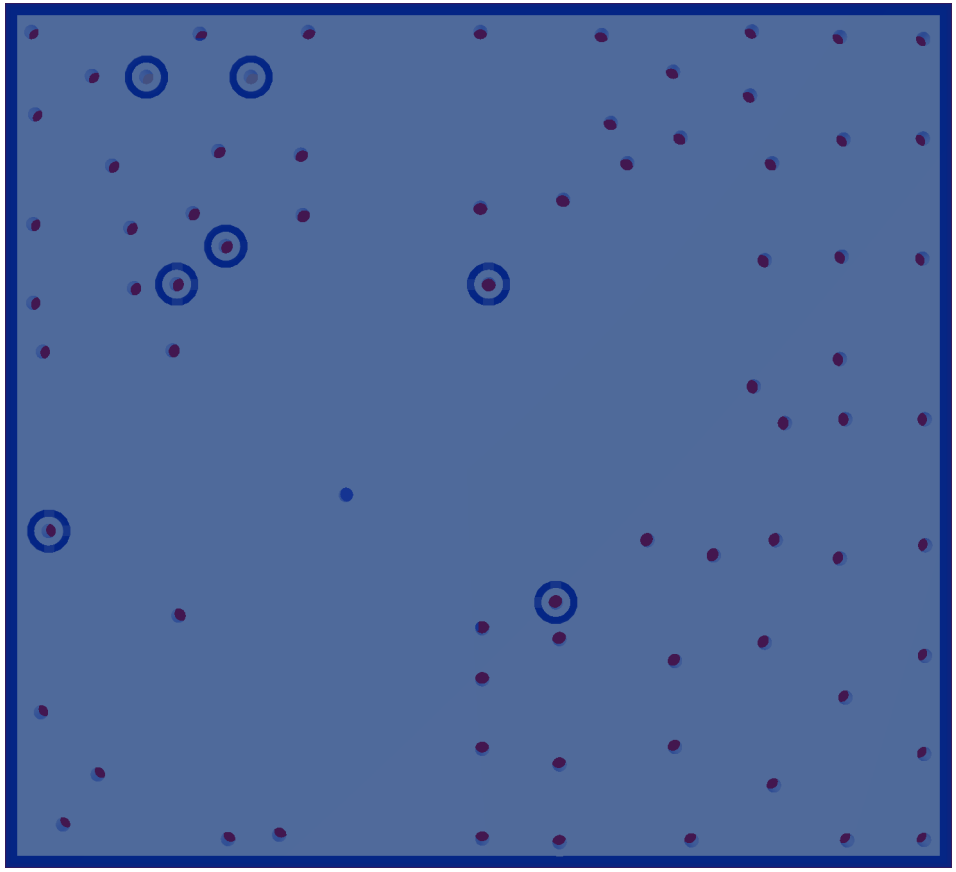
Realistic View Top Side



Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 16 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

NORDIC SEMICONDUCTOR

Realistic View Bottom Side



Title	nRF54LM20B-PAAA Reference Layout, 8 layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LM20B-PAAA	Revision: 1.0	
Date: 2026-11-19			Sheet 17 of 17	
File: nRF54LM20B-PAAA_Reference_Layout_8L_config1.PCBDwf				
Classification: Public				

NORDIC SEMICONDUCTOR